* Cache Memory - analyze all 3, how they work, determine line, size of mem, size of block, num unique tags
  + Direct mapping
  + Victim cache
    - Helps with direct mapping
    - Stores all ‘misses’
    - Improves efficiency, requires extra hardware
    - 1 problem
* Memory hierarchy
  + Pyramid
    - Top: registers
    - Cache
    - memory/RAM
    - Hard drives/other stuff
  + Speed, memory size, cost, efficiency
* Virtual vs physical memory - very slight(1 simple q)
* Methods of accessing units of data
  + Slide 5 of ch4 ppt
* RAM vs ROM
  + EEPROM vs EPROM
  + DRAM vs SRAM
* Hamming Code
  + Put in diagram, check for parity - mostly how it works, just understand, don’t need to draw
    - Odd flip bits
    - Add 1 to outside
  + Larger words by algorithm - important(8 bit only)
  + Calculating check bits from SEC - not as much
  + OVERVIEW of diagram - dont worry about diagram
    - Order of bits doesn’t matter
    - DEC has an extra bit outside of diagram
    - Then create an error
  + For venn diagram mostly need num of check bits, and num of errors that can be detected
    - Number of check bits = num for SEC +1
* Buses
  + Data
  + Address
  + Control
* Interrupts
  + Classes
    - Hardware, timer,
    - What happens
      * Pause program, do interrupt, return to program
      * Stored to stack(Program Counter)
* RTL and data path
  + Understand how data moves
  + Understand instruction cycle(from T1)
  + Drawing part
    - All registers connected to buss

Changed weights

Assignments and final project - 40%

Each exam - 30%